Docket No.	

COMBINED DECLARATION/POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

SEMICONDUCTOR SUBSTRATE, METHOD FOR FABRICATING THE SAME, AND METHOD FOR FABRICATING SEMICONDUCTOR DEIVCE the specification of which (check one) is attached hereto. was filed on Application Serial No. I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a). I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed: Prior Foreign Application(s) **Priority Claimed** 2003-011017 JAPAN 20/01/2003 _X_ Yes ___ No (Number) (Country) (Day/Month/Year Filed) ___ Yes ___ No (Day/Month/Year Filed) (Number) (Country) __ Yes ___ No (Day/Month/Year Filed) (Number) (Country)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Appln. Serial No.)	(Filing Date)	(Status-patented, pending, abandoned)
(Appln. Serial No.)	(Filing Date)	(Status-patented, pending, abandoned)
prosecute the patent appli and Trademark Office cor Jr. (Reg. No. 28,149); K 28,562); Paul Devinsky (I	ication identified above inected therewith: Raj Kenneth L. Cage (Reg Reg. No. 28,553); Edwa 139); Brian E. Ferguso	full power of substitution and revocation, to e and to transact all business in the U.S. Patent phael V. Lupo (Reg. No. 28,363); Jack Q. Lever No. 26,151); Stanislaus Aksman (Reg. No. ard E. Kubasiewicz (Reg. No. 30,020), Michael on (Reg. No. 36,801); Robert W. Zelnick (Reg. 37,136).
Please address all correspondent	ondence and telephone	calls to:
	Jack Q. Lever, Jr. McDERMOTT, WILL 600 Thirteenth Street, Washington, D.C. 20 (202) 756-8000	
instructions from Mark Office regard attorney and the undersign	aeda Patent Office ing this application wi ned. In the event of a	S. attorneys named herein to accept and follow as to any action to be taken in the Patent and thout direct communication between the U.S. change in the persons from whom instructions ill be so notified by the undersigned.
all statements made on it statements were made wit punishable by fine or imp	nformation and belief th the knowledge that virisonment, or both, und	e herein of my own knowledge are true and that are believed to be true; and further that these villful false statements and the like so made are ler Section 1001 of Title 18 of the United States jeopardize the validity of the application or any
Full name of sole or first i	nventor Yoshiharu H	IIDAKA

Post Office Address 5-40-7, Nanpeidai, Takatsuki-shi, Osaka 569-1042, Japan

Date January 8, 2004
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Docket No.: 60188-751 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Yoshiharu HIDAKA, et al. : Confirmation Number:

Serial No.: : Group Art Unit:

Filed: January 12, 2004 : Examiner: Unknown

For: SEMICONDUCTOR SUBSTRATE, METHOD FOR FABRICATING THE SAME, AND METHOD FOR FABRICATING SEMICONDUCTOR DEVICE

ASSOCIATE POWER OF ATTORNEY

Mail Stop Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The undersigned Principal Attorney of record hereby appoints the following Attorneys as his Associates with regard to the above-identified application: Steven W. Allis, Reg. No. 50,532; Stephen A. Becker, Reg. No. 26,527; John G. Bisbikis, Reg. No. 37,095; Richard E. Brown, Reg. No. 47,453; Daniel Bucca, Reg. No. 42,368; Kenneth L. Cage, Reg. No. 26,151; Jennifer Chen, Reg. No. 42,404; Bernard P. Codd, Reg. No. 46,429; Lawrence T. Cullen, Reg. No. 44,489; Paul Devinsky, Reg. No. 28,553; Margaret M. Duncan, Reg. No. 30.879: Shamita De. Etienne-Cummings, Reg. No. 46,072; Ramyar M. Farid, Reg. No. 46,692; Brian E. Ferguson, Reg. No. 36,801; Michael E. Fogarty, Reg. No. 36,139; John R. Fuisz, Reg. No. 37,327; Keith E. George, Reg. No. 34,111; Thomas A. Haag, Reg. No. 47,621; John A. Hankins, Reg. No. 32,029; Catherine Krupka, Reg. No. 46,227; Jack O. Lever, Reg. No. 28,149; Raphael V. Lupo, Reg. No. 28,363; Michael A. Messina, Reg. No. 33,424; Dawn L. Palmer, Reg. No. 41,238; Joseph H. Paquin, Jr., Reg. No. 31,647; Scott D. Paul, Reg. No. 42,984; William D. Pegg, Reg. No. 42,988; Robert L. Price, Reg. No. 22,685; Rubinson, Reg. No. 33,351; Brian K. Seidleck, Reg. No. 51,321; Joy Ann G. Serauskas, Reg. No. 27,952; Jiri F. Smetana, Reg. No. 52,456; David A. Spenard, Reg. No. 37.449; Arthur J. Steiner, Reg. No. 26,106; Michael D. Switzer, Reg. No. 39,552; David M. Tennant, Reg. No. 48,362; Judith L. Toffenetti, Reg. No. 39,048; Daniel S.

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Please continue to address all communications to the indersigned.

January 12, 2004

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